



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Patent Application of:)
Hiroaki NAKAOKA et al.) Group Art Unit: 2818
Serial No. 09/640,519) Examiner: David Vu
Filed: 08/17/2000) Confirmation No. 6287
For: METHOD OF FABRICATING SEMICONDUCTOR DEVICE) Date: March 10, 2004

REQUEST FOR ACKNOWLEDGMENT OF
CONSIDERATION OF DISCLOSED INFORMATION

Mail Stop Issue Fee
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

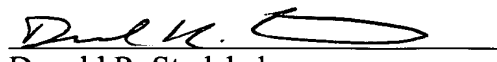
Sir:

An Information Disclosure Statement with Form PTO-1449 was filed in the above-captioned patent application on February 18, 2004. Applicants have not yet received back from the Examiner a copy of the Form PTO-1449 initialized to acknowledge the fact that the Examiner has considered the cited disclosed information.

The Examiner is requested to initial and return to the undersigned a copy of the subject Form PTO-1449.

Should there be any questions concerning this communication, please telephone the undersigned at the number set forth below.

Respectfully submitted,


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